

S/N Unknown

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	John S. Sadowsky et al.	Examiner:	Unknown
Serial No.:	Unknown	Group Art Unit:	Unknown
Filed:	Herewith	Docket:	884.A56US1
Title:	SYSTEMS AND METHODS FOR ADAPTIVE BIT LOADING IN A MULTIPLE ANTENNA ORTHOGONAL FREQUENCY DIVISION MULTIPLEXED COMMUNICATION SYSTEM		
Assignee:	Intel Corporation	Customer No:	21186

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

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Assignee: Intel Corporation

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Dkt: 884.A56US1 (INTEL)

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

JOHN S. SADOWSKY ET AL.

By their Representatives,

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Date Dec. 29, 2003

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"Express Mail" mailing label number: EV 332569264 US

Date of Deposit: December 29, 2003

This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to The Commissioner for Patents, Mail Stop Patent Application, P.O. Box 1450, Alexandria, VA 22313-1450.

<p>Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)</p>		<p style="text-align: center;"><i>Complete if Known</i></p> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 50%; padding: 2px;">Application Number</td> <td style="width: 50%; padding: 2px;">Unknown</td> </tr> <tr> <td style="padding: 2px;">Filing Date</td> <td style="padding: 2px;">Even Date Herewith</td> </tr> <tr> <td style="padding: 2px;">First Named Inventor</td> <td style="padding: 2px;">Sadowsky, John</td> </tr> <tr> <td style="padding: 2px;">Group Art Unit</td> <td style="padding: 2px;">Unknown</td> </tr> <tr> <td style="padding: 2px;">Examiner Name</td> <td style="padding: 2px;">Unknown</td> </tr> </table>						Application Number	Unknown	Filing Date	Even Date Herewith	First Named Inventor	Sadowsky, John	Group Art Unit	Unknown	Examiner Name	Unknown
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Sheet 1 of 1		Attorney Docket No: 884.A56US1															

US PATENT DOCUMENTS						
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS						
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS						
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		BANGERTER, BOYD, "High-Throughput Wireless LAN Air Interface", <u>Intel Technology Journal: Wireless Technologies</u> , Vol. 7, Issue 3, Available at http://developer.intel.com/technology/itj/index.htm , (August 19, 2003), 47-57				

EXAMINER

DATE CONSIDERED